

1/5

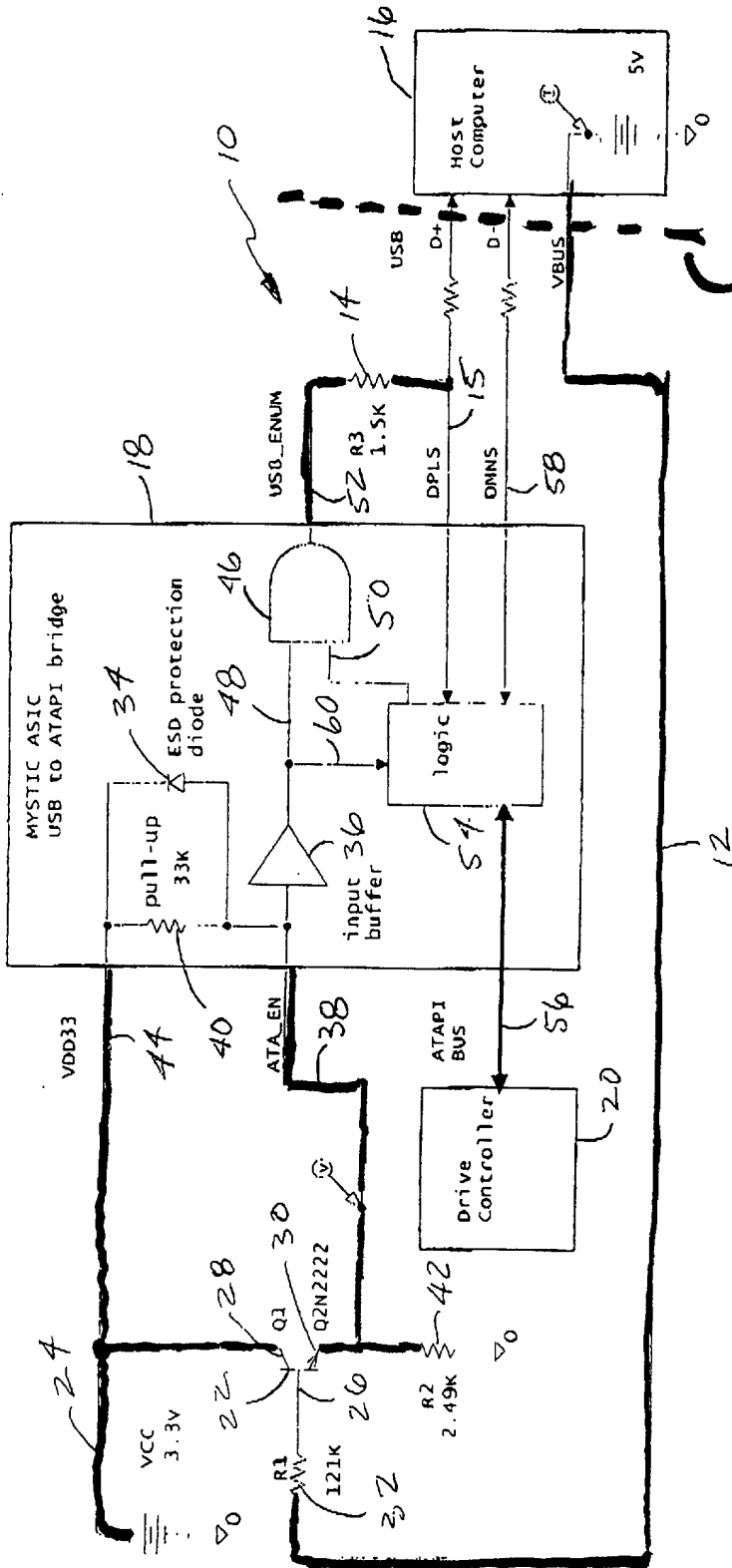


FIG. 1

2/5

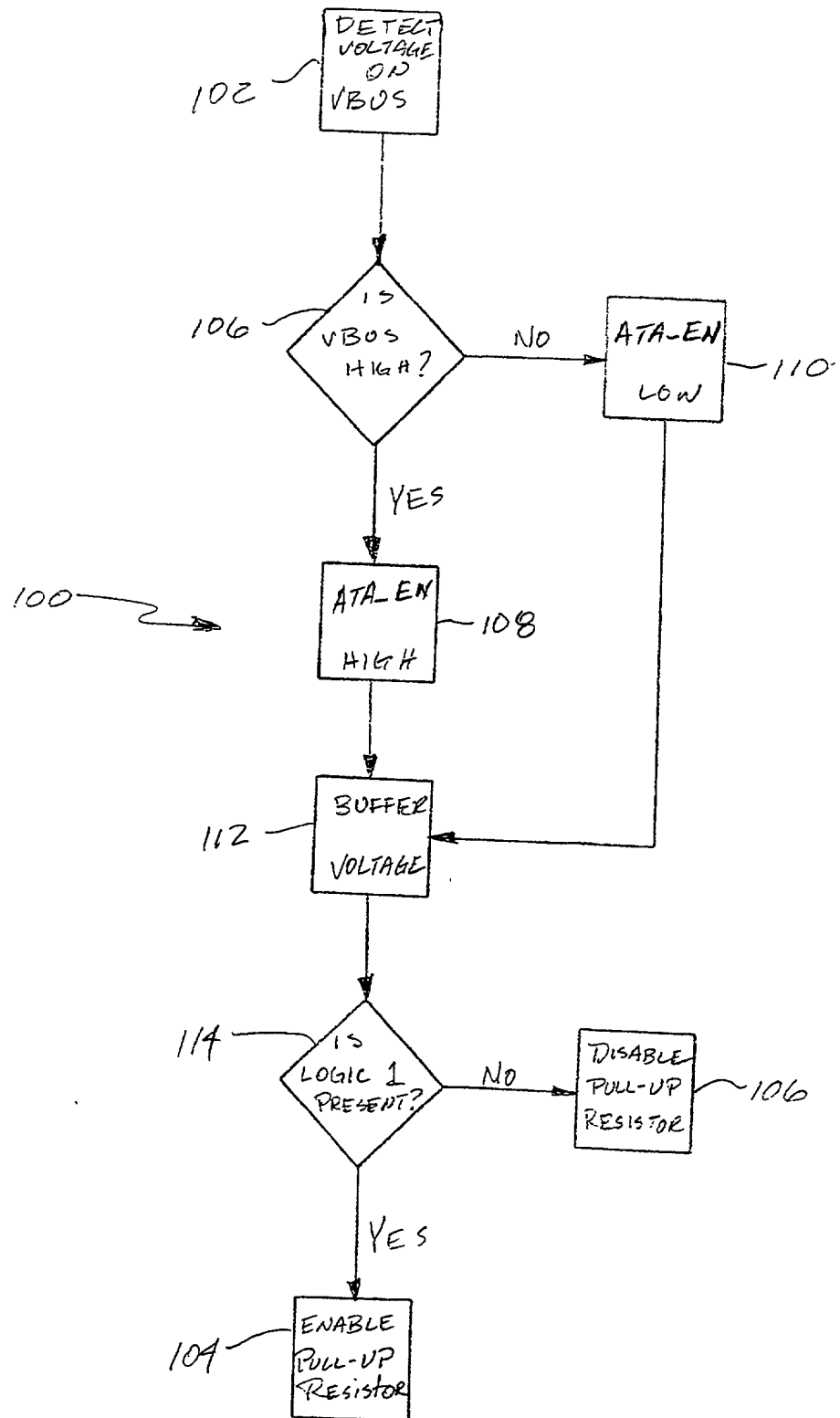
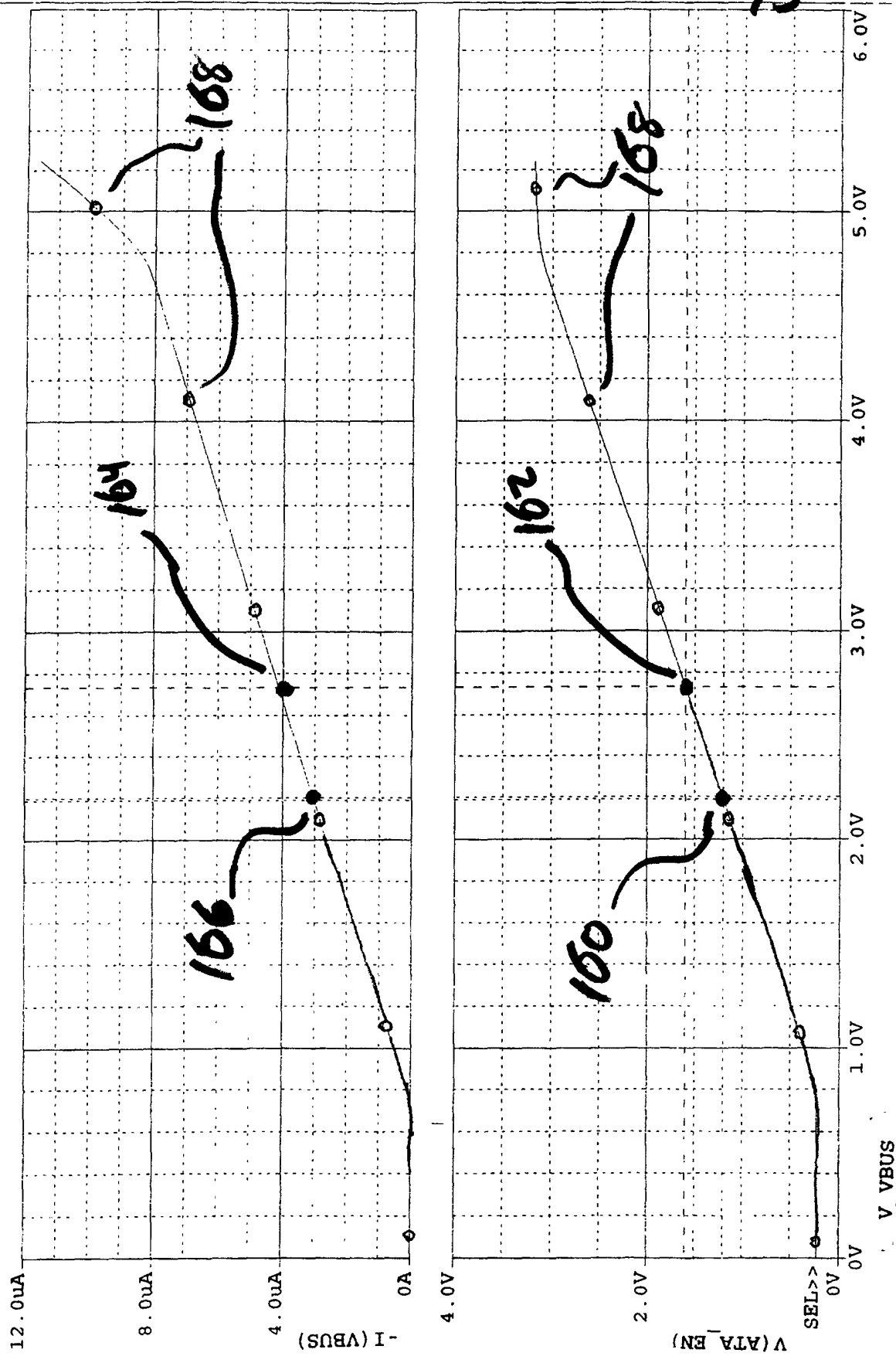


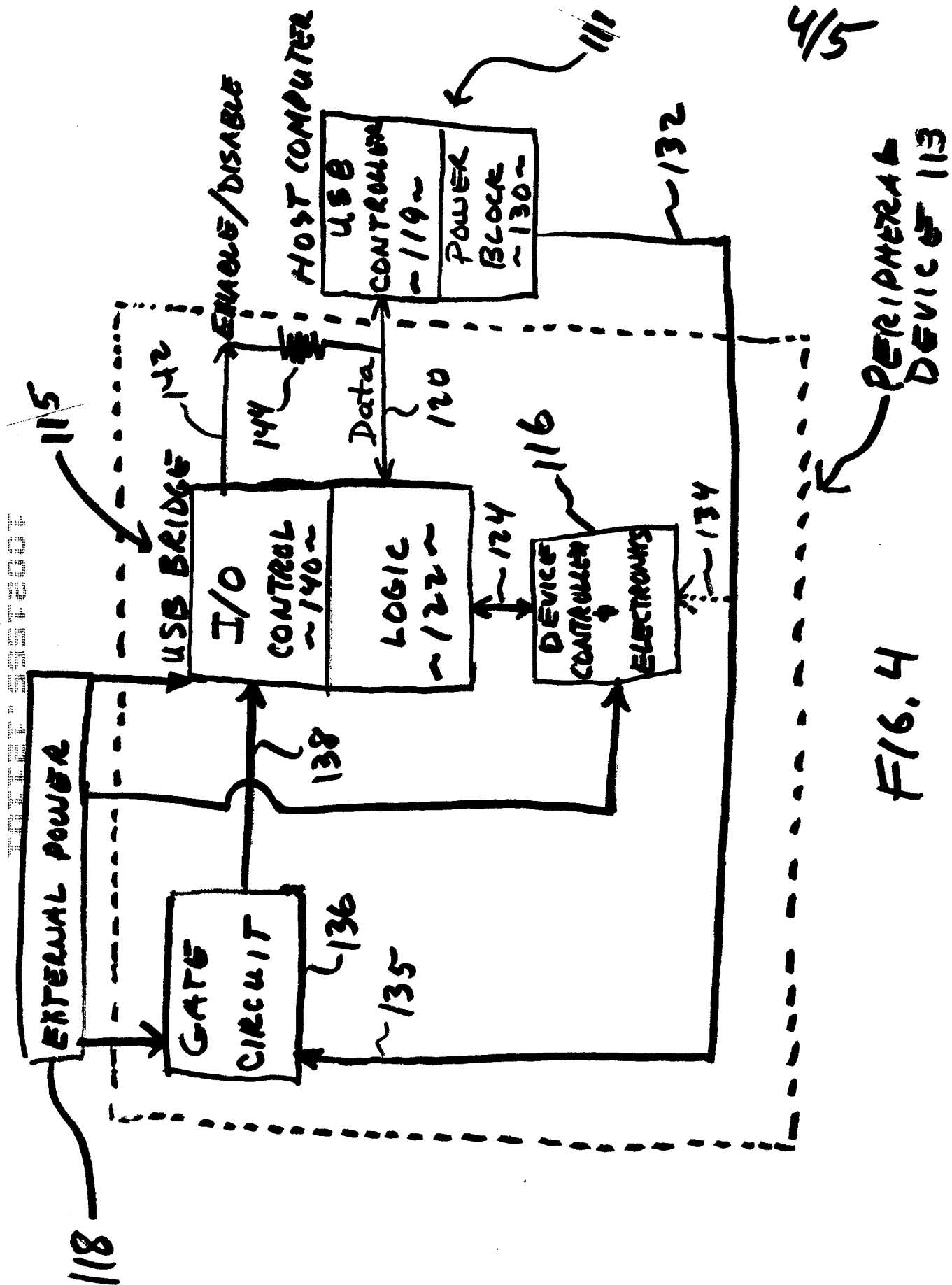
FIG. 2

Figure 16.3 shows the typical current-voltage characteristics of the device. The curves are plotted for various gate voltages (V_{GS}) ranging from 0V to 6V. The current (I_D) is plotted on the y-axis, ranging from 0A to 12.0uA. The voltage (V_{DS}) is plotted on the x-axis, ranging from 0V to 6.0V. The curves show that the current increases with increasing gate voltage and increasing drain voltage.



3/5

FIG. 3



EXT POWER	BRIDGE LOGIC	I/O CONTROL	PULL-UP RESISTOR	DATA LINE	POWER BUS	STATUS
NO	WORKING	1	ENABLE	TRANSMIT	YES/ YES	A
YES	WORKING	0	DISABLE	NON-ACTIVE	NO/LOW	B
—	DORMANT	OFF	N/A	NON-ACTIVE	—	C

TRI-STATE MATRIX

150

152

151

154

FIG. 5